

HW # 10.3

4. This is similar to the previous three exercises. The output is $\overline{(\overline{x}yz)}(\overline{x} + y + \overline{z})$.

6. We build these circuits up exactly as the expressions are built up. In part (b), for example, we use an *AND* gate to join the outputs of the inverter (which was applied to the output of the *OR* gate applied to x and y) and x .

